

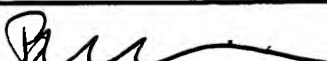
Substitute for form 1449A/PTO		Complete If Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Application Number	10/090,944
		Filing Date	3.4.02
		First Named Inventor	Paul Luse
		Art Unit	2186
		Examiner Name	PEIKARI
Sheet	1	of	2
		Attorney Docket Number	42P18502

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
BYP ✓	1	Adaptec - Adaptec SCSI RAID 2000S Support	
BYP ✓	2	Adaptec - Adaptec SCSI RAID 2005S Support	
BYP ✓	3	Adaptec - Motherboard Partners and Compatibility	
BYP ✓	4	Adaptec - Adaptec SCSI RAID 2005S Technical Specs	
BYP ✓	5	Adaptec - Adaptec Article: Zero Channel RAID Frequently Asked Questions	
BYP ✓	6	Intel Corp., Product Brief, Intel Server Board SCB2	
BYP ✓	7	Serial ATA: High Speed Serialized AT Attachment Revision 1.0, 29 August 2001	
BYP ✓	8	Super0, Super P3TDDR, User's Manual, Revision 1.0a	
BYP ✓	9	PCI Local Bus, PCI BIOS Specification, Revision 2.1 August 26, 1994	
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BYP ✓	11	PCI Local Bus, PCI Local Bus Specification, Revision 2.2, December 18, 1998	
BYP ✓	12	PCI Local Bus, Mini PCI Specification, Revision 1.0, October 25, 1999	
BYP ✓	13	PCI Local Bus, PCI-X Addendum to the PCI Local Bus Specification, Revision 1.0a, July 24, 2000	
BYP ✓	14	PCI Local Bus, PCI Local Bus Specification, Revision 2.3, March 29, 2002	

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Examiner Signature		Date Considered	10/29/04
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\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

†Applicant's unique citation designation number. ‡Applicant is to place a check mark here if English language Translation is attached.

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Sheet	2	of	2
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**Examiner  
Signature**

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